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## **OFFICE OF PETITIONS**

Docket No.: 060188-0031

**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Kentaro SHIOMI, et al. : Confirmation Number: 6677

Application No.: 09/779,440 : Group Art Unit: 2132

Filed: February 09, 2001 : Examiner: Jung W. Kim

For: LSI DESIGN METHOD AND VERIFICATION METHOD

## INFORMATION DISCLOSURE STATEMENT

Mail Stop Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the reference listed on the attached form PTO-1449. It is respectfully requested that the reference be expressly considered during the prosecution of this application, and that the reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The reference was first cited in a corresponding foreign office action and its relevance is discussed therein. A copy of the foreign office action is attached for the Examiner's information.

Application No.: 09/779,440

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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SHEET 1 OF 1 SERIAL NO. INFORMATION DISCLOSURE ATTY. DOCKET NO. 09/779,440 FAX RECEIVED 060188-0031 CITATION IN AN APPLICATION DEC 1 2 2006 APPLICANT Kentaro SHIOMi, et al. OFFICE OF PETITIONS **GROUP FILING DATE** (PTO-1449) February 09, 2001 2132 **U.S. PATENT DOCUMENTS** EXAMINER'S Document Number Publication Date Name of Patentee or Applicant of Cited Pages, Columns, Lines, Where NO. INITIALS MM-DD-YYYY Document Relevant Passages or Relevant Number-Kind Codes (# known) Figures Appear 12/05/2000 6157947 Wetenabe et al. US US US us US US บรั ŨS US US US US US UŞ FOREIGN PATENT DOCUMENTS **EXAMINER'S** Publication Date Foreign Patent Document Name of Patentee or Pages, Columns, Lines Translation INITIALS CITE Country Codes-Number 4-Kind Codes (If known) MM-DD-YYYY Applicant of Cited Document Where Relevant Yes Figures Appear JP 05-128197 05/25/1993 Mesaharu X JP 11-224284 08/17/1999 Yoshiski et al. X 11/24/1998 JP 10-312311 Yasuto OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, **EXAMINER'S** INITIALS journal, serial, symposium, catalog, etc.), date, page(a), volume-leaue number(s), publisher, city and/or country where CITE badaildug NO. Narita et al. "Board Level Simulation with R4400 Model," Study Report in Information Processing Society of Japan, Dec 1995, pp 77-84, Vol 95, No 119(DA-78) Takeyame et al, "A High Speed Logic Verification System of Large Scale Digital Designs," Research Report of Information Processing Society of Japan, Jan 1995, pp 9-16, Vol 95, No 6(DA-73) Notice of Reasons for Rejection Issued by Japanese Patent Office (Translation) **EXAMINER** 

EXAMINER: Initial if reference considered, whether or not cliation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. include copy of this form with next communication to applicant.

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